

Electronic Acknowledgement Receipt

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Application Number:	10751324
Patent Number:	7176713
Confirmation Number:	1991
Petition Issued Date:	August 31,2011
Title of Invention:	INTEGRATED CIRCUITS WITH RAM AND ROM FABRICATION OPTIONS
First Named Inventor/Applicant Name:	Raminda Udaya Madurawe
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Filer:	Jose S. Garcia/Donna Petford
Filer Authorized By:	Jose S. Garcia
Attorney Docket Number:	
Receipt Date:	31-AUG-2011
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Payment Type	Electronic Funds Transfer
Payment was successfully received in RAM	\$2620
RAM confirmation Number	19571
Deposit Account	
Authorized User	

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part /.zip	Pages (if appl.)
1	Petition automatically granted by EFS	TIER-0003_7176713_sb0066e_f ill-- auto_grant_for_Unintentionally_delayed_payment_of_Main_Fees.pdf	841478 1d23e2544efcb8791c67b5171bab5d534c7 d8087	no	3

Warnings:

Information:

2	Fee Worksheet (SB06)	fee-info.pdf	32154 ed7b65c5220a7ae37224c6176cd87cbab6 d9c18	no	2
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Warnings:

Information:

Total Files Size (in bytes):

873632

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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